

#### Technical areas

- Modeling and simulation of established semiconductor device, including FinFETs, GAA FETs, ultra-thin SOI devices, optoelectronic devices, TFTs, sensors, power electronic devices, and organic electronic devices.
- Modeling and simulation of emerging devices including tunnel FETs, SETs, spintronic devices, straintronic devices, bio-electronic devices, and new material-based devices for various applications.
- Modeling and simulation of interconnects, noise and parasitic effects.
- Modeling and simulation of all sorts of semiconductor processes, including first principles material design, and growth simulation of nanoscale fabrication.
- Advances in fundamental aspects of device modeling and simulation, including of charge, spin, and thermal transport, of collective states including spin/magnetic and charge, and of fluctuation, noise, and reliability.
- Numerical methods and algorithms, including grid generation, user-interface, and visualization.
- Compact modeling for circuit simulation, including low-power, high frequency, and power electronics applications.
- Process/device/circuit co-simulation in context with system design and verification, including for emerging devices.
- Modeling and simulation of equipment, topography, lithography.
- Benchmarking, calibration, and verification of simulators.

### Conference Venue

Palacio de congresos de Granada Paseo del Violón, 18006, Granada (Spain) https://goo.gl/maps/ptkwd6cJSXjGygfh8

### Important dates

Two-page abstract\*: April 8, 2022 Notification of acceptance: May 29, 2022 Extended four-page final paper\*: June 24, 2022

Early registration: July 22, 2022 Late registration: August 19, 2022

\*Submissions should be made through the website.

# **Technical Co-Sponsorship**

- Electron Device Society of the Institute of Electrical and Electronic Engineers
- Universidad de Granada

# **Organizing Committee**

F. Gámiz, University of Granada (General Chair)

C. Medina-Bailón, University of Granada (Technical Program Chair)



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