SISPAD2017 CONFERENCE SCHEDULE

Date	Room	Morning	Afternoon 1	Afternoon 2	Evening
Thursday Sept. 7	A	9:00–11:35 Session 1: Plenary (3 Invited)	13:00–14:50 Session 2: Band Structure I : Interface Traps, Doping and Phonons (1 Invited + 4 Regulars)	15:10–17:00 Session 4: Nanowire (1 Invited + 4 Regulars)	18:00–20:00 Reception
	B		13:30–14:50 Session 3: Computational Methodology (4 Regulars)	15:40–17:00 Session 5: Material and Geometry Impact (4 Regulars)	
Friday Sept. 8	A	9:00–11:30 Session 6: Reliability (1 Invited + 5 Regulars)	13:00–14:50 Session 8: Non-volatile Memory (1 Invited + 4 Regulars)		
	B	9:30–11:30 Session 7: Power Devices (5 Regulars)	13:30–14:50 Session 9: Interconnects (4 Regulars)		
	C			15:00–17:00 Poster Session (27 Posters)	
Saturday Sept. 9	A	9:00–11:30 Session 10: Transport (1 Invited + 5 Regulars)	13:00–14:40 Session 12: Future Devices (5 Regulars)	15:00–16:50 Session 14: SOI, Double-Gate and FinFET (5 Regulars)	
	B	9:30–11:30 Session 11: High Speed Switching Devices and Noise (5 Regulars)	13:00–14:20 Session 13: Band Structure II : 2D and Nano Systems (4 Regulars)		