Workshop on Carrier Transport in Nano-Transistors: Theories and Experiments

Monday, September 8, 2014

This workshop is intended to provide a forum for promoting our understanding of carrier transport in ultimately-scaled transistors and collecting missing piece of the puzzle. The downscaling of CMOS transistors in integrated circuits continues with an introduction of new device structures and new materials. To take advantage of their potentials and find the limits of the miniaturization, a deep understanding of new transport phenomena emerging in nanoscale transistors is indispensable both from the theoretical and experimental viewpoints.

This workshop is planned to consist of theoretical and experimental talk of leading experts in the field for each topic. A list of invited speakers is given below.

Hideaki Tsuchiya, Kobe University, Japan Yoshinari Kamakura, Osaka University, Japan

Program

09:15 - 09:20	Welcome Hideaki Tsuchiya
	Kobe University (Japan)
09:20 - 10:00	Experimental Aspects of Carrier Transport Properties in III-V and III-V-OI MOSFETs Shin'ichi Takagi, Sang-Hyeon Kim, and Mitsuru Takenaka
10:00 - 10:40	University of Tokyo (Japan) Electron Transport in Thin-Body InGaAs-OI MOSFETs: A Theoretical Viewpoint Tomislav Suligoj
	University of Zagreb (Croatia)
10:40 - 11:00	Break
11:00 - 11:40	Carrier Transport in Ge MOSFETs: An Experimental Viewpoint Akira Toriumi
11:40 - 12:20	University of Tokyo (Japan)
11.40 - 12.20	Carrier Transport in Nanoscale MOSFETs and Tunnel FETs: Fundamental Aspects and Design Implications
	David Esseni
	University of Udine (Italy)
	Chivelenty of Camb (Mary)
12:20 - 13:50	Lunch
12:20 - 13:50 13:50 - 14:30	
	Lunch Experimental Study on Carrier Transport in Ultrathin-Body MOSFETs
	Lunch Experimental Study on Carrier Transport in Ultrathin-Body MOSFETs Ken Uchida Keio University (Japan) Carrier Transport in Nanowire MOSFETs: An Experimental Viewpoint Toshiro Hiramoto
13:50 - 14:30 14:30 - 15:10	Lunch Experimental Study on Carrier Transport in Ultrathin-Body MOSFETs Ken Uchida Keio University (Japan) Carrier Transport in Nanowire MOSFETs: An Experimental Viewpoint Toshiro Hiramoto University of Tokyo (Japan)
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